# DATAFLOW NETWORK OF CAL-ACTORS FOR ALL-PAIR SHORTEST PATHS SEARCH 

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CAL is a high-level actor-based dataflow language [1-6]. A CAL-program is defined as a network of actors that interact and communicate by sending and receiving data (tokens) along data lossless and order preserving communication channels. An actor is a computational entity that consists of input and output ports, state variables, actions, and a scheduler. Actors are executed in parallel. When an actor is fired, it consumes tokens from input ports, changes the internal state and produces tokens on output ports. An action is a piece of computation that an actor performs in the firing process. An actor may contain any number of actions. When an actor is being firing, it selects one of them based on the availability of input tokens and optionally based on conditions relating to the values of tokens and state variables. An action guard enables conditional action firing. A finite state machine allows actions to be scheduled according to the current state of the actor and action priorities.

The shortest path problem in a weighted directed cyclic graph [7-13] has many application domains. Although a variety of shortest path algorithms in different settings exist, the scaling and parallelization of the problem on multi-processor systems are still open. Analysis and simulation of the Floyd-Warshall $(F W)$ and Blocked Floyd-Warshall ( $B F W$ ) all-pair shortest paths algorithms [10-11] have shown that the $B F W$ is more suitable for parallelization and speeding up the computations. Moreover, it supports spatial data locality within block and leads to the reduction of data transfer in hierarchical memory and to decreasing the overall execution time. The authors of work [14] proposed an advanced heterogeneous blocked all-pair shortest paths algorithm. A drawback of the algorithms is the realization of fork-join parallelism, which makes them slower against network algorithms. Such computer architectures as multi-core systems include a set of cores and a hierarchical memory consisting of local and shared cache levels, which differ on memory capacity and data transfer time delays. The cores read and write data through fast local caches, and therefore efficiently execute algorithms which support spatial and temporal locality in big data processing.

The advantage of the blocked Floyd-Warshall (BFW) Algorithm 2 proposed in $[10,11,14]$ is the introduction of spatial data locality due to decomposing matrix $D[N \times N]$ of graph edge weights into blocks of size $S \times S$ each and forming a blocked matrix $B[M \times M]$, where $M=N / S$ is the number of blocks per row. The algorithm provides sequential data locality within each block. Its main loop has $M$ iterations, $S$ times less compared to $F W$. Every iteration of the loop recalculates each block once and tries to update each element $S$ times. Totally, every element of matrix $D$ has $N$ attempts to update. The block recalculation is performed locally by using from one to two other blocks. Algorithm 2, $B C A$ implements the $F W$ algorithm, recalculates block $B^{1}$ consuming two additional blocks $B^{2}$ and $B^{3}$. It is possible to choose the block size
in such a way as the processed blocks could be deployed in fast caches simultaneously, which reduces the data traffic between memory levels. The $B F W$ algorithm operation time crucially depends on which level of memory the matrix $D$ fits completely in. If in level L1 and/or L2 do, the algorithm runs quickly. If level L3 does, the data transfer time delay is higher, and the algorithm runs slower. The slowest case takes place when the size of matrix $D$ is larger than the size of cache L3; elements of $D$ are read from and written to main memory many times, which produces big cache pressure and consumes much time.

To speed up the shortest paths algorithm search due to asynchronous behavior [15-17], the paper introduces a dataflow network of actors, which is realized in the CAL language [1]. It also presents a CAL-engine implemented in the $\mathrm{C} / \mathrm{C}++$ language as a multi-threaded application on multi-core systems. The CAL-engine efficiently accounts for features of the dataflow network.

Algorithm 1: Blocked Floyd-Warshall (BFW)

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        Input: A number N of graph vertices
Input: A matrix W of graph edge weights
Input: A size S of block
Output: A blocked matrix B of path distances
    M\leftarrowN/S B[M\timesM]\leftarrowW[N\timesN]
    for }m\leftarrow1\mathrm{ to }M\mathrm{ do
        BCA (B
        for }i\leftarrow1\mathrm{ to }M\mathrm{ do
        if }i\not=m\mathrm{ then
                BCA (Bi,m, Bi,m},\mp@subsup{B}{\textrm{m},\textrm{m}}{})\quad// \textrm{C}
                BCA ( }\mp@subsup{B}{\textrm{m},\textrm{i}}{,},\mp@subsup{B}{\textrm{m},\textrm{m}}{},\mp@subsup{B}{\textrm{m},\textrm{i}}{})\quad//\textrm{C}
    for }i\leftarrow1\mathrm{ to }M\mathrm{ do
        if i\not=m}\mathrm{ then
                for }j\leftarrow1\mathrm{ to }M\mathrm{ do
                if j\not=m}\mathrm{ then
                BCA ( }\mp@subsup{B}{\textrm{i},\textrm{j},}{,},\mp@subsup{B}{\textrm{i},\textrm{m}}{},\mp@subsup{B}{\textrm{m},\textrm{j}}{})\quad//\textrm{P}
    return B
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Algorithm 2: Block calculation ( $B C A$ )
Input: A size $S$ of block
Input: Blocks $B^{1}, B^{2}$ and $B^{3}$
Output: $B^{1}$ - recalculated block
for $k \leftarrow 1$ to $S$ do for $i \leftarrow 1$ to $S$ do for $j \leftarrow 1$ to $S$ do $\operatorname{sum} \leftarrow b_{\mathrm{i}, \mathrm{k}}^{2}+b_{\mathrm{k}, \mathrm{j}}^{3}$ if $b^{1}{ }_{\mathrm{i}, \mathrm{j}}>\operatorname{sum}$ then $b^{1}{ }_{\mathrm{i}, \mathrm{j}} \leftarrow \operatorname{sum}$
return $B^{1}$

Let assume the $D$ matrix be mapped to a $B[3 \times 3]$ blocked matrix. In this case, we can model each block $B_{\text {rc }}$ by a CAL actor for which $M$ and $B$ are global variables. Figure 1 shows the block-actor interface of four input ports, $L_{\mathrm{r} 1}, L_{\mathrm{r} 2}, L_{1 \mathrm{c}}$ and $L_{2 \mathrm{c}}$, and two output ports, $L_{\mathrm{r}}$ and $L_{\mathrm{c}}$. The input ports receive tokens from output ports of other actors, which describe the calculation level of associated blocks. The output ports $L_{\mathrm{r}}$ and $L_{\mathrm{c}}$ describe the $B_{\mathrm{rc}}$ block calculation level that is used by other blocks located in row $r$ and column $c$. We recognize two types of block-actors: diagonal and non-diagonal. Algorithm 3 depicts the behavior of CAL-actor Block_0_0 that models the calculation of diagonal block $B_{00}$. Input ports $L \_0 \_1$ and $L_{-} 0 \_2$ describe the calculation level of the $B_{01}$ and $B_{02}$ blocks in row 0 . Input ports $L \_1 \_0$ and $L \_2 \_0$ describe the calculation level of the $B_{10}$ and $B_{20}$ blocks in column 0 . Output ports Lrow and $L c o l$ describe the calculation level of block $B_{00}$. State variables Lev, Row and Col describe the calculation level, row and column respectively of block $B_{00}$. The Block_0_0 actor contains three actions: one diagonal and two peripherals. The input and output tokens, and the guard condition of
the diagonal action distinct from those of the peripheral one. The diagonal action has no input token and has two output tokens associated with the Lrow and Lcol ports and getting the value of state variable Lev. The guard condition requires Lev to be equal to Row. The action body increments the block calculation level and calls the BCA function to recalculate the diagonal block. The action is fired when the block calculation level is equal to its row and column. Each of the two peripheral actions has three input and no output tokens. In the first action, two input tokens $L 01$ and $L 10$ arrive from ports $L \_0 \_1$ and $L \_1 \_0$, and the third token is a constant $k$. The guard condition requires Lev be lower than the calculation levels $L 01$ and $L 10$. The action body increments the block calculation level and calls the BCA function to recalculate the $B_{\mathrm{rc}}$ block over the $B_{\mathrm{rk}}$ and $B_{\mathrm{kc}}$ blocks. The peripheral action is fired when the input tokens have arrived and the block $B_{\mathrm{rc}}$ calculation level is lower than those of blocks $B_{\mathrm{rk}}$ and $B_{\mathrm{kc}}$.


Figure 1 - Interface of the $A_{\mathrm{rc}}$ CAL-actor that models the calculation of block $B_{\mathrm{rc}}$ in matrix $D[3 \times 3]$
Algorithm 4 depicts the CAL-actor Block_0_1 that models the calculation of nondiagonal block $B_{01}$. Input ports $L \_0 \_0$ and $L \_0 \_2$ describe the calculation level of $B_{00}$ and $B_{02}$ blocks in row 0 . Input ports $L \_1 \_1$ and $L \_2 \_1$ describe the calculation level of $B_{11}$ and $B_{21}$ blocks in column 1. Output ports Lrow and Lcol describe the calculation level of blocks $B_{01}$. State variables Lev, Row and Col describe the calculation level, row and column of block $B_{01}$.

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Algorithm 3: CAL-actor Block_0_0 calculating diagonal block \(B_{00}\) of matrix \(B[3 \times 3]\)
actor Block_D () int \(L \_0 \_1\), int \(L \_0 \_2\), int \(L \_1 \_0\), int \(L \_2 \_0==>\) int \(L r o w\), int \(L c o l\) :
    int Lev: \(=0\);
    int Row: \(=0\);
    int Col: \(=0\);
    Diagonal: action ==> Lrow: [Lev], Lcol: [Lev]
    guard Lev \(=\) Row
    do
        Lev:= Lev + 1;
        \(B C A(B[\) Row \(* M+C o l], B[\) Row \(* M+C o l], B[R o w * M+C o l])\);
    end
    Peripheral_1: action L_0_1: [L01], L_1_0: [L10], const\#1:[k] ==>
    guard \(L 01>=L e v+1\) and \(L 10>=L e v+1\)
    do
```

```
    Lev:=Lev + 1;
    BCA (B[Row * M + Col], B[Row * M +k], B[k*M+Col ]);
    end
    Peripheral_2: action L_0_2: [L02], L_2_0: [L20], const#2:[k] ==>
    guard L02>=Lev + 1 and L20>=Lev + 1
    do
        Lev:=Lev + 1;
        BCA (B[Row * M + Col], B[Row * M +k], B[k*M + Col]);
    end
end
```

Algorithm 4: CAL- actor Block_0_1 calculating non-diagonal block $B_{01}$ of matrix $B[3 \times 3]$

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actor Block_N() int \(L_{-} 0 \_0\), int \(L_{-} 0 \_2\), int \(L_{-} 1 \_1\), int \(L_{-} 2 \_1==>\) int Lrow, int Lcol:
    int Lev:= 0;
    int Row: \(=0\);
    int Col:= 1 ;
    Cross1: action L_1_1: [L11] ==> Lrow: [Lev]
    guard \(\mathrm{Col}=L 11-1\) and \(L e v=L 11-1\)
    do
        Lev := Lev + 1;
        \(B C A(B[R o w * M+C o l], B[\) Row \(* M+C o l], B[C o l ~ * M+C o l]) ;\)
    end
    Cross2: action L_0_0: [L00] ==> Lcol: [Lev]
    guard Row \(=L 00-1\) and \(L e v=L 00-1\)
    do
        Lev:= Lev +1 ;
        \(B C A(B[\) Row \(* M+C o l], B[\) Row \(* M+\) Row \(], B[\) Row \(* M+C o l])\);
    end
    Peripheral_3: action L_0_2: [L02], \(L \_2 \_1:[L 21]\), const\#2:[k] ==>
    guard \(L 02>=L e v+1\) and \(L 21>=L e v+1\)
    do
        Lev:= Lev + 1;
        \(B C A(B[\) Row \(* \mathrm{M}+\mathrm{Col}], B[\) Row \(* M+k], B[k * M+C o l])\);
    end
end
```

Actor Block_0_1 contains three actions: Cross1, Cross2 and Peripheral. The Cross1 action has an input token $L 11$ associated with port $L_{-} 1 \_1$, and has an output token associated with port Lrow and getting the value from state variable Lev. The guard condition requires Col and Lev be equal to $L 11-1$. The action body increments the block calculation level and calls the $B C A$ function to recalculate block $B_{01}$ over block $B_{11}$. The Crossl action is fired when a token arrives at its input port and its guard condition evaluates to true. The behavior of Cross2 action is similar to those of Cross1
action. The behavior of the Peripheral action in a non-diagonal actor is the same as those in the diagonal one.

Composing the actors into a network together with setting connections among input and output ports and locating buffers at the input ports establish a dataflow network. The network is a coordination model of the concurrent actor computation. Fig. 2 shows a network of nine actors for the $B[3 \times 3]$ matrix. Three actors are diagonal, and six actors are non-diagonal. Right output ports connect actors along rows. Bottom output ports connect actors along columns. All actors can be fired simultaneously.


Figure 2 - CAL network of two types block actors for $B[3 \times 3]$ matrix
To implement the behavior of actors, actions and dataflow network on a multicore system, we have developed a C/C++ based CAL language engine. Every component needed for the CAL runtime system is implemented by means of an appropriate C/C++ class of objects. As a result, a CAL network and each of its actors are instantiated via complex data structures and a set of methods in C/C++. All actors operate concurrently. One actor sends a flow of tokens to other actors through buffers and ports. Since several actors update common variables in buffers, the engine synchronizes the actor's communications and the data processing.

We have generated dataflow actor networks for various block-matrix size, M and have done experiments on randomly generated weighted complete graphs of 1200, 2400 and 3600 vertices. Experimental results shown in fig. 3 are obtained on the i79750 h processor: 6 cores, 12 logical processors, 2.60 GHz of frequency, and 16 GB of main memory. Fig. 3 compares the speedup the multi-threaded dataflow CALnetworks implementing BFW have given against the single-thread FW implementation. On the block-matrix of 1200 graph vertices, the highest speedup of about 5 has been obtained. On 2400 graph vertices providing higher load of cores, the CALnetwork has given the speedup larger than the number of cores (more than 6 ) on matrices $B[5 \times 5], B[6 \times 6]$ and $B[7 \times 7]$. On 3600 graph vertices the speedup is even larger (about 7) on matrix $B[6 \times 6]$. On 3600 graph vertices the speedup is even larger (about 7) on matrix $B[6 \times 6]$.

Conclusion. The paper has considered the solving of all-pair shortest paths problem on large graphs by means of the dataflow computation model of the CAL actor language. The proposed CAL language engine-based actor, action and dataflow network models constitute a high-performance scalable parallel implementation of blocked shortest paths algorithms on multicore systems.


Figure 3 - Speedup (vertical axis) of multi-threaded dataflow network $B F W$ against single-thread $F W$ vs. $M$ (horizontal axis) on graph size of 1200, 2400 and 3600 vertices on i7-9750h processor

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